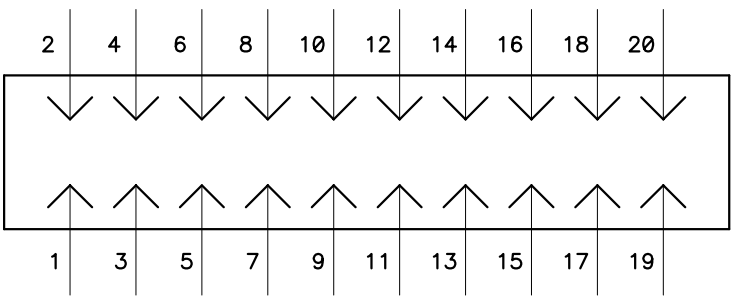
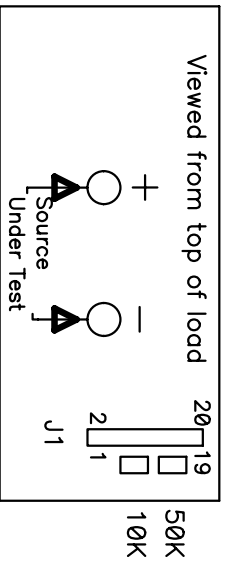


Connector shown from top view. Pins shown as seen on electronic load.

Note J1 - Pin 2 and the (-) source under test are connected, never have current flowing through this path. All control signals should be connected to J1-pin 2. Only the source under test should be connected to the power test terminal (+) and (-).



All slave electronic loads follow what the master does. Never mix model types of electronic loads. This design is used for higher power at low voltage. Use Cap across terminal to stop oscillation of electronic loads when using long connection lead to loads.

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Master Slave Operation (4) LOADS

Title		Master Slave Operation (4) LOADS	
Size	Number	EE301/151	Rev
A			A
Date	6-15-2000	Drawn by	D. WEBER
Filename	Master-Slave.sch	Sheet	1 of 1