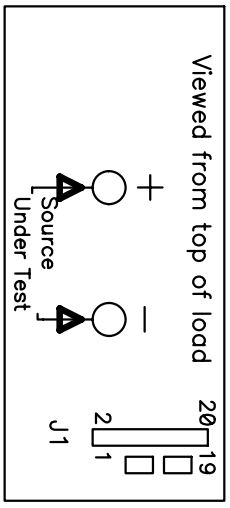


MORE CURRENT



Note J1 - Pin 2 and the (-) source under test are connected, never have current flowing through this path. All control signals should be connected to J1-pin 2. Only the source under test should be connected to the power test terminal (+) and (-).

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EXECUTIVE ENGINEERING

Title External Quasi POWER CABLE (4) LOADS

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| A | X00-0000-002 | A |

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| Date | 9-15-2004 | Drawn by | D. WEBER |
| Filename | quasi-power-parallel.sch | Sheet | 1 of 1 |